



SAINTGITS
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SCE/2022-23/03/433

Date: 09.03.2023

ABSTRACT

Saintgits College of Engineering (Autonomous) - Academics - Changes in the Final Year Curriculum of B.Tech. Electronics and Communication Engineering Programme (2020 Curriculum Scheme) - Approved - Resolution of the Academic Council Implemented - Orders Issued

Read: Minutes of 8th meeting of the Academic Council held on 10th February 2023

ORDER

The 8th Academic Council meeting of Saintgits College of Engineering (Autonomous) vide agenda AC/008/A19 of the paper read above has resolved to approve the following changes in the final year curriculum of B.Tech. programme in Electronics and Communication Engineering under the 2020 Curriculum Scheme.

Semester	Core/Elective Course Basket	Course as per approved Curriculum (2020 scheme)	Approved New Course	Nature of Change
7	Programme Core	Wireless Communication	Microwaves and Antennas	Replacement
7	Programme Elective II	Optoelectronics Devices	Instrumentation	Replacement
7	Programme Elective II	Error Control Codes	-	Removal
7	Programme Elective II	Antenna and Wave Propagation	-	Removal
8	Programme Elective IV	Microwave Devices and Circuits	-	Removal
8	Programme Core	Instrumentation	Wireless Communication	Replacement

Orders are issued accordingly to implement the changes with effect from 2023-24 academic year. The revised final year curriculum of B.Tech. programme in Electronics and Communication Engineering is enclosed with this order for the follow-up action by all the concerned offices.


PRINCIPAL

Encl: The revised final year curriculum of B.Tech. programme in Electronics and Communication Engineering

Copy to:

- Chairman-Governing Body
- Dean-Research, Dean-Planning & Monitoring, Dean-Academics, Controller of Examinations, Dty. Controller of Examinations
- Associate Dean (Academics & Compliance), HOD-Electronics Engineering, CDC Convener, Manager-Office Administration
- Faculty Members-Department of Electronics Engineering, Academic Module Coordinator, Associate Academic Module Coordinator, Timetable Coordinator, Assistant Professor-AMC, Academic Office File
(for circulation among faculty members and office staff)

Prof. (Dr.) Josephkunju Paul C
Principal
Saintgits College of Engineering (Autonomous)
Kottukulam Hills, Pathamuttom P.O.
Kottayam - 686 532, Kerala

**SAINTGITS COLLEGE OF ENGINEERING
(AUTONOMOUS)**

Kottukulam Hills, Pathamuttom P.O.,
Kottayam - 686 532, Kerala
Tel: +91 481 2436169, 2436170, 2430349
mail@saintgits.org
www.saintgits.org



Revised Final Year Curriculum of B.Tech. Electronics and Communication Engineering Programme (2020 Curriculum Scheme)
approved by 8th Academic Council Meeting held on 10th February 2023

Semester	Slot	Course Code	Course Name	L-T-P	CIE Marks	ESE Marks	Total Marks	Hrs.	Credits
VII	A	20ECT401	Microwaves and Antennas	2-1-0	50	100	150	3	3
Programme Elective-II									
VII	B	20ECT411	Optical Fiber Communication	2-1-0	50	100	150	3	3
VII	B	20ECT421	Computer Networks	2-1-0	50	100	150	3	3
VII	B	20ECT431	Instrumentation	2-1-0	50	100	150	3	3
VII	B	20ECT451	Machine Learning	2-1-0	50	100	150	3	3
VII	B	20ECT471	DSP Architectures	2-1-0	50	100	150	3	3
Open Elective									
VII	C	20ECT413	Mechatronics	2-1-0	50	100	150	3	3
VII	C	20ECT423	Biomedical Instrumentation	2-1-0	50	100	150	3	3
VII	C	20ECT433	Electronics Hardware for Engineers	2-1-0	50	100	150	3	3
VII	C	20ECT443	IoT and Applications	2-1-0	50	100	150	3	3
VII	C	20ECT453	Entertainment Electronics	2-1-0	50	100	150	3	3
VII	D	20MCN401	Industrial Safety Engineering	2-1-0	50	50	100	3	P/F
VII	S	20ECL401	Electromagnetics Lab	0-0-3	75	75	150	3	2
VII	T	20ECQ401	Seminar	0-0-3	100	-	100	3	2
VII	U	20ECD401	Project Phase-I	0-0-6	100	-	100	6	2
M/H/R	Minor/Honours/Remedial				50	100	150	4*	4**
								24/28	15/19/23

This list is enclosed with Order SCE/2022-23/03/433 dated 9th March 2023 issued by Principal



Revised Final Year Curriculum of B.Tech. Electronics and Communication Engineering Programme (2020 Curriculum Scheme)
approved by 8th Academic Council Meeting held on 10th February 2023

Semester	Slot	Course Code	Course Name	L-T-P	GIE Marks	ESE Marks	Total Marks	Hrs.	Credits
VIII	A	20ECT402	Wireless Communication	3-0-0	50	100	150	3	3
Programme Elective-III									
VIII	B	20ECT412	Biomedical Engineering	2-1-0	50	100	150	3	3
VIII	B	20ECT422	Satellite Communication	2-1-0	50	100	150	3	3
VIII	B	20ECT432	Secure Communication	2-1-0	50	100	150	3	3
VIII	B	20ECT442	Pattern Recognition	2-1-0	50	100	150	3	3
VIII	B	20ECT452	RF Circuit Design	2-1-0	50	100	150	3	3
VIII	B	20ECT462	Mixed Signal Circuit Design	2-1-0	50	100	150	3	3
VIII	B	20ECT472	Entrepreneurship	2-1-0	50	100	150	3	3
Programme Elective-IV									
VIII	C	20ECT414	Modern Communication Systems	2-1-0	50	100	150	3	3
VIII	C	20ECT424	Real Time Operating Systems	2-1-0	50	100	150	3	3
VIII	C	20ECT434	Adaptive Signal Processing	2-1-0	50	100	150	3	3
VIII	C	20ECT454	Speech and Audio Processing	2-1-0	50	100	150	3	3
VIII	C	20ECT464	Analog CMOS Design	2-1-0	50	100	150	3	3
VIII	C	20ECT474	Robotics	2-1-0	50	100	150	3	3

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Semester	Slot	Course Code	Course Name	L-T-P	CIE Marks	ESE Marks	Total Marks	Hrs.	Credits
Programme Elective-V									
VIII	D	20ECT416	Mechatronics	2-1-0	50	100	150	3	3
VIII	D	20ECT426	Optimization Techniques	2-1-0	50	100	150	3	3
VIII	D	20ECT436	Computer Vision	2-1-0	50	100	150	3	3
VIII	D	20ECT446	Low Power VLSI	2-1-0	50	100	150	3	3
VIII	D	20ECT456	Internet of Things	2-1-0	50	100	150	3	3
VIII	D	20ECT466	Renewable Energy Systems	2-1-0	50	100	150	3	3
VIII	D	20ECT476	Organic Electronics	2-1-0	50	100	150	3	3
VIII	E	20ECD404	Comprehensive Course Viva	1-0-0	-	50	50	1	1
VIII	U	20ECD402	Project Phase-II	0-0-12	75	75	150	12	4
M/H/R		Value Added Course/Minor/Honours/Remedial			50	100	150	4*	4**
								25/29	17/21/25



This list is enclosed with Order SCE/2022-23/03/433 dated 9th March 2023 issued by Principal